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Class	Issue Classification

PATENT NUMBER

U.S. UTILITY Patent Application

12W O.I.P.E.
SCANNED A/S Q.A.

PATENT DATE

APPLICATION NO. 09/664910	CONT/PRIOR:	CLASS 710 714	SUBCLASS 718	ART UNIT 2100 2133	EXAMINER Dipakkumar Gandhi
APPLICANTS TOMO HELBERT RANDY Bonella					

Memory module and memory component built-in self test
TITLE APPLICANT(S):

PTO-2040
12/99

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ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)		
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)	
714	718	714	7185	
INTERNATIONAL CLASSIFICATION		365	201	
G 11 G 29/05				

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets	Drwg.	Figs. Drwg.	Print Fig.	Total Claims 64
	2	2	2	2	Print Claim for O.G. 59
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	Dipakkumar Gandhi 12/24/03 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED 12-30-03	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	ALBERT DECADY SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100 Decady 12/24/03 (Primary Examiner) (Date)			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	8-Jaw (Legal Instruments Examiner) (Date)			Amount Due \$1330	Date Paid

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